

CLAIMS

1. An image pickup device comprising: a first
solid-state image pickup element which accumulates first
5 information electric charges generated in response to a
first object image in a plurality of light reception pixels;
a first drive circuit which obtains a first image signal by
driving the first solid-state image pickup element; a second
solid-state image pickup element which accumulates second
10 information electric charges generated in response to a
second object image in a plurality of light reception
pixels; a second drive circuit which obtains a second image
signal by driving the second solid-state image pickup
element; a timing control circuit which determines timing of
15 vertical scanning and horizontal scanning of the first and
second solid-state image pickup elements; and a selector
circuit which selectively supplies a predetermined power
supply voltage to the first and second solid-state image
pickup elements, wherein the first and second solid-state
20 image pickup elements operate in a time-sharing manner, and
the power supply voltage is supplied to the solid-state
image pickup element which is in an operating state.

2. The image pickup device according to claim 1,
25 wherein the selector circuit overlaps a part of a period in
which the power supply voltage is supplied to one of the
first and second solid-state image pickup elements with

respect to a period in which the power supply voltage is supplied to the other one of the first and second solid-state image pickup elements.

5 3. The image pickup device according to claim 1,
wherein the first solid-state image pickup element comprises
a first capacitance which takes in and accumulates the first
information electric charges which are transferred and
output and a first output amplifier which takes out a change
10 in potential of the first capacitance according to an
accumulated electric charge quantity of the first
information electric charges and outputs the first image
signal, the second solid-state image pickup element
comprises a second capacitance which takes in and
15 accumulates the second information electric charges which
are transferred and output and a second output amplifier
which takes out a change in potential of the second
capacitance according to an accumulated electric charge
quantity of the second information electric charges and
20 outputs the second image signal, and the selector circuit
supplies the power supply voltage to the output amplifier of
the solid-state image pickup element which is in an
operating state of the first and second output amplifiers.

25 4. The image pickup device according to claim 3,
wherein the selector circuit overlaps a part of a period in
which the power supply voltage is supplied to one of the

first and second output amplifiers with respect to a period in which the power supply voltage is supplied to the other one of the first and second output amplifiers.

5 5. The image pickup device according to claim 1,
further comprising an output selector circuit which takes in
the first and second image signals and selectively outputs
the first and second image signals to a processing circuit
on a next stage in synchronization with operation timing of
10 the first and second solid-state image pickup elements,
wherein the output selector circuit has a plurality of input
paths respectively corresponding to the first and second
image signals, each input path operates upon receiving the
power supply voltage, and the selector circuit selectively
15 supplies the power supply voltage to each of the plurality
of input paths in synchronization with the operation timing
of the first and second solid-state image pickup elements.

20 6. The image pickup device according to claim 5,
wherein the selector circuit overlaps a part of a period in
which the power supply voltage is supplied to one of the
plurality of input paths with respect to a period in which
the power supply voltage is supplied to the other one of the
plurality of input paths.